



IC2 - Compact PCI

This course is an extension to the PCI course

Objectives

- This course highlights the new hardware features / constraints of CompactPCI with regard to PCI.
- Hardware and software additional requirements to support hot swap are explained.
- Implementation of a non-transparent 21554 intel PCI-to-PCI bridge frequently used in Compact PCI systems.

A more detailed course description is available on request at info@ac6-training.com

Prerequisites

- Knowledge of PCI 3.0: see our course reference [IC1](#)

Plan

THE CompactPCI SPECIFICATION

- Backplane design rules
- Board design rules
- Clocking
- Interrupt lines
- Electrical requirements on the system board

HOT SWAP

- Architecture model, compliance levels
- Hot swap additional signals
- Insertion & extraction sequences

THE NON-TRANSPARENT 21554 PCI-TO-PCI BRIDGE

- Address decoding
- Data path inside the bridge
- Consequences of PCI transaction ordering rules
- I2O specification objectives, implementation in the 21554

Renseignements pratiques

Duration : 1 days
Cost : 700 € HT



SARL au capital de 15400€ - SIRET 449 597 103 00026 - RCS Nanterre - NAF 722C - Centre de Formation : 19, rue Pierre Curie - 92400 Courbevoie
Siège social et administration : 21, rue Pierre Curie - 92400 Courbevoie - Tél. 01 41 16 80 10 - Fax. 01 41 16 07 78

Last site update: Tue 22 May 2012 10:50:29 AM CEST

<http://www.ac6-formation.com/>