



```
.calendar { width: 100%; border-collapse: collapse; } .calendar th, .calendar td { border: 1px solid #ddd; padding: 8px; } .calendar th { background-color: #f2f2f2; text-align: center; } .calendar tr:nth-child(even) { background-color: #f9f9f9; } .calendar tr:hover { background-color: #ddd; } .calendar .cal_header { background-color: #4CAF50; color: white; } .calendar .cal_category { background-color: #2196F3; color: white; } .calendar .cal_col_header { background-color: #f2f2f2; } .calendar .cal_c_even { background-color: #ffffff; } .calendar .cal_c_odd { background-color: #f9f9f9; } .calendar .cal_c_even_s_even, .calendar .cal_c_even_s_odd, .calendar .cal_c_odd_s_even, .calendar .cal_c_odd_s_odd { background-color: #ffffff; } .calendar a { color: #2196F3; text-decoration: none; } .calendar a:hover { text-decoration: underline; }
```

Safety and security				
Course	Duration	2025		
		May	June	July
C1 - Effective MISRA C	2 days	on request		
C2 - MISRA Compliance for Project Managers	1 day	on request		
SEC1 - Developing C/C++ Secure Embedded Systems	18 hours	on request		
SEC10 - Cyber Resilience Act and Embedded Systems	1 day	on request		
SEC2 - Advanced Embedded Systems Security	12 hours	on request		
SEC12 - Comprehensive Secure Systems Programming	30 hours	on request		
SEC6 - Embedded Security for NXP i.MX-based processors	2 days	on request		
SEC7 - ARM TrustZone for Cortex-M based devices	1 day	on request		
SEC8 - Secured Embedded Linux Platform Build	2 days	on request		
SEC9 - Advanced Embedded Linux Security	3 days	on request		
C8 - Critical Systems Safety	3 days	20-22- Paris		

Languages				
Course	Duration	2025		
		May	June	July
L2 - C language for Embedded MCUs	4 days	on request		
L3 - Embedded C++	3 days	on request		
L4 - Industrial Java	4 days	on request		
L4G - Java for Android	2 days	on request		
L5 - Java Temps Réel	3 days	on request		
L8 - Python	4 days	on request		
L9 - OpenCL	3 days	on request		
L10 - Embedded Modern C++ Programming	2 days	on request		
L30 - Classic and Modern C++ for Embedded Systems	5 days	on request		
E1 - Eclipse	3 days	on request		
RT1 - Real Time and Multi-Core programming	5 days	on request		
STG - STM32 + FreeRTOS + LwIP	5 days		30/06-04/07- Paris	

V1 - VHDL Language Basics	4 days	on request		
V2 - Advanced VHDL for FPGA	3 days	on request		
V3 - Design with SystemC	4 days	on request		
Methods				
Course	Duration	2025		
		May	June	July
C7 - UML Real-Time	4 days	on request		
C8 - Critical Systems Safety	3 days	20-22- Paris		
C9 - Software Architecture with UML	4 days	on request		
E1 - Eclipse	3 days	on request		
RT1 - Real Time and Multi-Core programming	5 days	on request		
RT3 - FreeRTOS Real Time Programming	3 days	on request		
Real-Time				
Course	Duration	2025		
		May	June	July
MC4 - Multi-Core Programming with OSEK/VDX and AutoSAR	3 days	on request		
RT1 - Real Time and Multi-Core programming	5 days	on request		
RT3 - FreeRTOS Real Time Programming	3 days	on request		
RT5 - Zephyr RTOS Programming	5 days	on request		
RT6 - Real Time Programming with Eclipse ThreadX	3 days	on request		
C7 - UML Real-Time	4 days	on request		
C8 - Critical Systems Safety	3 days	20-22- Paris		
C9 - Software Architecture with UML	4 days	on request		
D4 - Real-time Linux	4 days	on request		
IOT1 - Internet of Things (IOT) on Microcontrollers	3 days	on request		
L5 - Java Temps Réel	3 days	on request		
STG - STM32 + FreeRTOS + LwIP	5 days		30/06-04/07- Paris	
TI3 - Cortex M4 Texas Instruments Implementation and TI-RTOS	4 days	on request		

FPGA				
Course	Duration	2025		
		May	June	July
ALT1 - CYCLONE-V CORTEX-A9 HARD PROCESSOR SYSTEM	5 days		<i>on request</i>	
H1 - Lattice Mico32 FPGA embedded processor	3 days		<i>on request</i>	
H2 - Lattice Diamond	2 days		<i>on request</i>	
HX4 - AMD (Xilinx) - Microblaze implementation	2 days		<i>on request</i>	
HX5 - AMD Zynq All Programmable SoC: Hardware and Software Design	2 days		<i>on request</i>	
MSP - Microchip SmartFusion2 Programming	3 days		<i>on request</i>	
RV1 - RISC-V Architecture	3 days		<i>on request</i>	
V0 - Programmable components fundamentals	2 days		<i>on request</i>	
V1 - VHDL Language Basics	4 days		<i>on request</i>	
V2 - Advanced VHDL for FPGA	3 days		<i>on request</i>	
V3 - Design with SystemC	4 days		<i>on request</i>	