



Programmable Logic						
Course	Duration	Cost	2024			
			May	June	July	August
ALT1 - CYCLONE-V CORTEX-A9 HARD PROCESSOR SYSTEM	5 days	2790 € HT				<i>on request</i>
H1 - Lattice Mico32 FPGA embedded processor	3 days	2180 € HT				<i>on request</i>
H2 - Lattice Diamond	2 days	1880 € HT				<i>on request</i>
HX4 - AMD (Xilinx) - Microblaze implementation	2 days	1970 € HT				<i>on request</i>
HX5 - AMD Zynq All Programmable SoC: Hardware and Software Design	2 days	2160 € HT				<i>on request</i>
MSP - Microchip SmartFusion2 Programming	3 days	2400 € HT				<i>on request</i>
RV1 - RISC-V Architecture	3 days	2620 € HT				<i>on request</i>
V0 - Programmable components fundamentals	2 days	2070 € HT				<i>on request</i>
V1 - VHDL Language Basics	4 days	2740 € HT				<i>on request</i>
V2 - Advanced VHDL for FPGA	3 days	2510 € HT				<i>on request</i>
V3 - Design with SystemC	4 days	2490 € HT				<i>on request</i>