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Courses on ARM cores

ACSYS offers a large set of courses on ARM processor cores.
Each course details both hardware and software implementation of these cores.
Programming examples are provided to clarify the operation of complex assembly instructions and to explain the parameterizing of the ARM linker.

AAA ARM Cortex-A and R Architecture (v7/v8) It provides the prerequisites needed to start learning the various ARMv7 A&R specific cores. It also briefly outlines the ARMv8 64bit architecture. 4 days **Inquiry**

AAA4 ARM Cortex-M Architecture (v7/v8) It provides the global understanding of the ARMv7 and v8 Cortex-M architecture needed to efficiently program MCU based on such cores. 4 days **Inquiry**

RA0 Cortex-A9 implementation 4 days **Inquiry**

RA1 Cortex-A5 implementation 3 days **Inquiry**

RA2 Cortex-A9 implementation 4 days **Inquiry**

RA3 Cortex-A15 implementation 4 days **Inquiry**

RA4 Cortex-A7 implementation 4 days **Inquiry**

RA5 Cortex-A17 implementation 4 days **Inquiry**

RA6 CORTEX-A53 implementation, ARM Architecture V8 4 days **Inquiry**

RA7 CORTEX-A72 implementation, ARM Architecture V8 4 days **Inquiry**

RA8 CORTEX-A7 implementation, ARM Architecture V8 4 days **Inquiry**

RA9 CORTEX-A8 implementation, ARM Architecture V8 4 days **Inquiry**

RC1 NEON programming 2 days **Inquiry**

RC2 NEON-V8 programming 2 days **Inquiry**

RM0 Cortex-M0 / Cortex-M0+ implementation 2 days **Inquiry**

RM1 Cortex-M1 implementation 3 days **Inquiry**

RM2 Cortex-M3 implementation 4 days **Inquiry**

RM3 AXI3 / AXI4 INTERCONNECT 2 days **Inquiry**

RM4 Cortex-M4 implementation 4 days **Inquiry**

RM5 Cortex-M4 / Cortex-M7 implementation 4 days **Inquiry**

RM6 Cortex-M33 implementation 4 days **Inquiry**

RP0 Cortex-R4 implementation 3 days **Inquiry**

RP1 Cortex-R5 implementation 3 days **Inquiry**

RP2 Cortex-R8 implementation 3 days **Inquiry**

RP3 ARM Cortex-R52/R52+ implementation and software design This course provides a deep dive into the ARM v8-R architecture and Cortex-R52/R52+ processors, focusing on pipeline behavior, exception handling, and memory management. Participants will explore advanced features like virtualization, safety mechanisms, and synchronization techniques, essential for developing robust real-time systems. 3 days **Inquiry**