Trainings Freescale ARM CPUs: Courses on Freescale i.MX and Kinetis SoCs

Freescale ARM CPUs

Courses on Freescale i.MX and Kinetis SoCs

ACSYS offers a large set of courses on Freescale processors.
Each course details both hardware and software implementation of these processors.
Examples are provided to explain low level programming, which is needed to understand the boot program.
Regarding the i.MX family, an additional day covering Linux porting or Windows Embedded porting may be appended to the processor course.

You can see detailed course descriptions of the various trainings by using the above navigation bar. You can also click on course identifiers in the following course briefs hereafter.

FA1 - i.MX27 implementation + LTIB This course describes the i.MX27 multimedia processor and Linux Target Image Builder tool
FA2 - i.MX31 implementation + LTIB This course describes the i.MX31 multimedia processor and Linux Target Image Builder tool
FA3 - i.MX51 Implementation + LTIB This course describes the i.MX51 multimedia processor and Linux Target Image Builder tool

FA4 - i.MX6 This course describes the i.MX6 Dual and Quad core SoC

FA5 - i.MX28 + LTIB This course describes the i.MX28 processor family and Linux Target Image Builder tool

FK1 - Freescale Kinetis MCU Implementation This course covers all MCUs belonging to the Kinetis family K10, K20, K30, K40 and K60