



Freescale DSPs

Courses on Freescale 16- /24-bit DSPs

ACSYS offers courses on Freescale DSPs.

Each course details both hardware and software implementation of these processors.

Examples are provided to explain low level programming, which is needed to implement algorithms efficiently.

You can see detailed course descriptions of the various trainings by using the above navigation bar. You can also click on course identifiers in the following course briefs hereafter.

FD1 - DSP568XX implementation This course covers the 568XX 16-bit DSP Freescale family

FD2 - DSP563XX implementation This course covers the 563XX 24-bit DSP Freescale family