



Marvell bridges

Courses on Marvell Discovery PowerPC companion bridges

ACSYS offers courses on Marvell Discovery bridges.

These bridges are generally used as companion devices for PowerPC 7XX and 7XXX CPUs.

Each course details both hardware and software implementation of these cores.

Programming examples are provided to clarify the operation of complex peripherals.

You can see detailed course descriptions of the various trainings by using the above navigation bar. You can also click on course identifiers in the following course briefs hereafter.

- [MV1 - MARVELL MV6446X implementation](#) This course covers Marvell Discovery III devices
[MV2 - MARVELL MV64560 implementation](#) This course covers Marvell Discovery V devices
[MV3 - MARVELL MV64660 implementation](#) This course covers Marvell Discovery VI devices