Trainings Languages: Langages de programmation pour l'embarqué et le temps réel

Languages

Langages de programmation pour l'embarqué et le temps réel

ac6-formation propose de vous former aux différents langages utilisés en informatique industrielle et embarquée. Nous vous proposons des cours sur les langages de développement C, C++ et Java. Contrairement aux cours génériques, tous nos cours sont adaptés à la programmation en environnement industriel et embarqué, avec des exercices sur des environnements embarqués.

You can see detailed course descriptions of the various trainings by using the above navigation bar. You can also click on course identifiers in the following course briefs hereafter.

Cours principaux

L2 - Embedded C programming Le langage C pour les systèmes embarqués
L3 - Embedded C++ programming Le langage C++ pour les systèmes embarqués
L4 - Java for industrial computing Le développement d'applications industrielles en Java(TM)
L5 - Real time Java Programmation temps réel en Java(TM)
L8 - Python Programming with the Python Language
Learn to program in an OS independent way using the Python language.

L9 - OpenCL Parallel programming with OpenCL-1.1
High Performance Computing (HPC) is more and more frequent in embedded systems, for graphics rendering, virtual reality of parallel computing. The OpenCL language allows to program in a more or less hardware-independent way complex parallel algorithms that will be able to run on various hardware platforms.

Autres cours

E1 - Eclipse Utilisation de l'environnement de développement Eclipse pour C, C++ et Java(TM)
RT1 - Real Time and Multi-Core programming Programming Linux real-time and multi-core systems, avoiding common pitfalls
Real-time and embedded code, especially targetting multicore processors, cannot be effectively tested; it must be validated before coding. This training help you master mutitask and real-time programming of multi-core processors, understanding how to effectively solve problems using the primitives provided by the underlying Operating System.

V1 - Le langage VHDL Programmation de FPGAs en VHDL
V2 - Design with SystemC System Design and Simulation with SystemC
Embedded electronic systems are more and more complex and designing them more difficult. Thus designing the hardware and the software separately becomes quite impractical. The SystemC language has been designed to design and simulate entire systems, both the hardware and software parts, even before its partitioning.